



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
 United States Patent and Trademark Office  
 Address: COMMISSIONER FOR PATENTS  
 P.O. Box 1450  
 Alexandria, Virginia 22313-1450  
 www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 9793

|                            |                                       |              |                        |                                  |
|----------------------------|---------------------------------------|--------------|------------------------|----------------------------------|
| SERIAL NUMBER<br>10760,489 | FILING DATE<br>01/21/2004<br><br>RULE | CLASS<br>327 | GROUP ART UNIT<br>2816 | ATTORNEY<br>DOCKET NO.<br>01-543 |
|----------------------------|---------------------------------------|--------------|------------------------|----------------------------------|

APPLICANTS

Kenji Ito, Anjo-city, JAPAN;  
 Takuya Harada, Kariya-city, JAPAN;  
 Hirofumi Isomura, Kariya-city, JAPAN;

\*\* CONTINUING DATA \*\*\*\*\*  
*None*  
*OK*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*  
 JAPAN 2003-13492 01/22/2003  
*Yes*  
*OK*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
 \*\* 04/28/2004

|  |  |                              |                        |                       |                            |
|--|--|------------------------------|------------------------|-----------------------|----------------------------|
| Foreign Priority claimed<br>35 USC 119 (a-d) conditions met<br>Verified and Acknowledged | <input checked="" type="checkbox"/> yes <input type="checkbox"/> no<br><input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after<br>Allowance<br><i>OK</i><br>Examiner's Signature Initials | STATE OR<br>COUNTRY<br>JAPAN | SHEETS<br>DRAWING<br>7 | TOTAL<br>CLAIMS<br>13 | INDEPENDENT<br>CLAIMS<br>3 |
|--|--|------------------------------|------------------------|-----------------------|----------------------------|

ADDRESS  
 23400  
 POSZ LAW GROUP, PLC  
 12040 SOUTH LAKES DRIVE  
 SUITE 101  
 RESTON , VA  
 20191

TITLE  
 Semiconductor integrated circuit device having a sampling signal generation circuit

|                            |   |  |
|----------------------------|---|--|
| FILING FEE<br><br>RECEIVED | FEES: Authority has been given in Paper<br>No. _____ to charge/credit DEPOSIT ACCOUNT<br>No. _____ for following: | <input type="checkbox"/> All Fees<br><input type="checkbox"/> 1.16 Fees ( Filing )<br><input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) |
|----------------------------|---|--|

|     |  |   |
|-----|--|---|
| 770 |  | <input type="checkbox"/> 1.18 Fees ( Issue )<br><input type="checkbox"/> Other _____<br><input type="checkbox"/> Credit _____ |
|-----|--|---|